

For details of tests and checks and acceptance criteria see detailed diagrams

Results of Operator Checks and Programmable Tests should go to Dbase

8. Power Reset button Verify LEDs Test VME Access 10. Test Serial EPROM Load with Ser Nr, Date etc Jumper for Write Protect Test FPGA loading Insert CFlash Card Power Reset Verify LEDs for Load Done Flashing Clocks 12. Run Test Bench Programs: Exercise FPGA Registers Read Temperatures Read Voltages Scan DACs and OptoRx settings capture data 13. Final Tests Box up