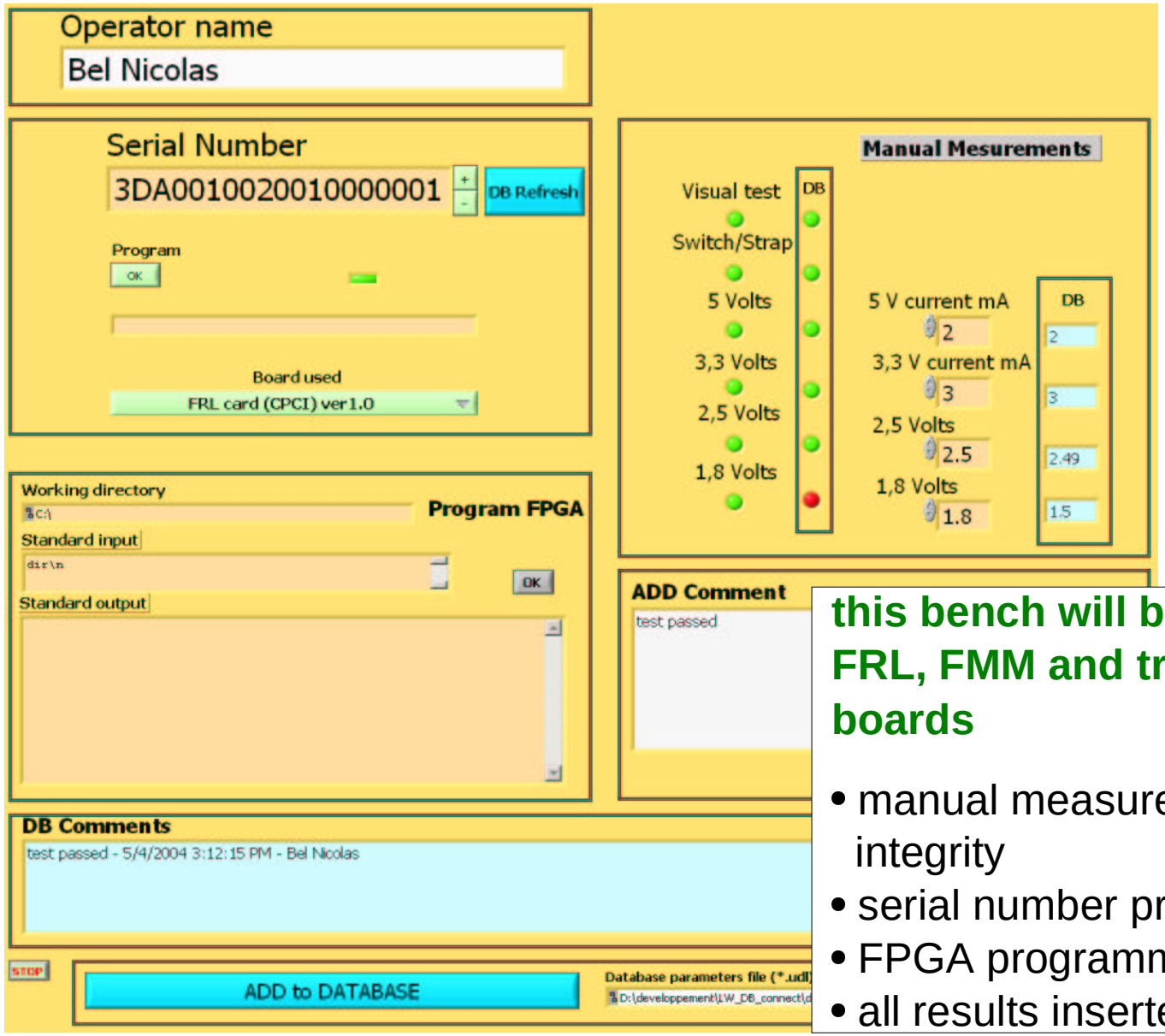
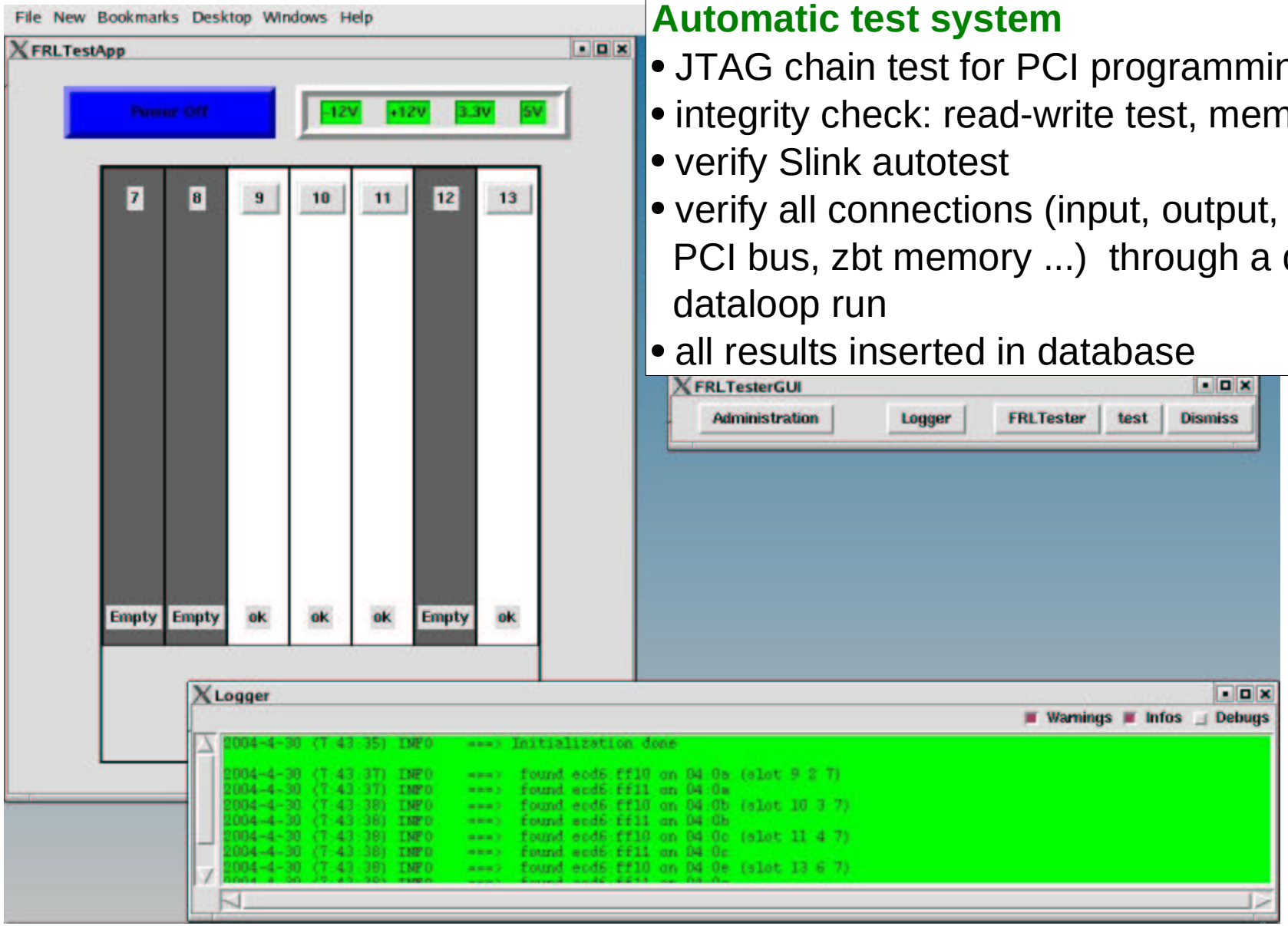
FRL SERIES: test bench 1



this bench will be used for Slink64, FRL, FMM and trigger distributor boards

- manual measurements of electric integrity
- serial number programming
- FPGA programming
- all results inserted in database

FRL SERIES: test bench 2



Automatic test system

- JTAG chain test for PCI programming
- integrity check: read-write test, memory test
- verify Slink autotest
- verify all connections (input, output, internal PCI bus, zbt memory ...) through a quick dataloop run
- all results inserted in database