

## Project Monitor Form

<b>Project:</b> CMS FED <b>Date:</b> Friday 24 Mar-2006	<b>PMF number:</b> 60 <b>Sheet:</b> 1 of 2
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Project Implementation phase.

**FED Production**

Board Deliveries to CMS: Remains on schedule.  
 Total at CERN is now 238 boards.  
 And another batch of ~ 64 was shipped yesterday.

**CMS Tracker FED Final Production Cumulative Delivery Schedule**  
(NB does not include Pre-Series or Prototype FED deliveries)

	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov
Planned at CERN						0	25	50	100	150	200	250	300	350	400	450	475	500	500	500
Actual at CERN					0	0	40	75	110	174	238	238	238	238	238	238	238	238	238	238

Production:

Proceeding to plan. Expect last assembly run in April/May.  
 All free issue parts to complete production now with eXception, incl TTCrx. (except ORx see below)

Minor problems in Assembly Test. html file generation was upset by inadvertent characters in some text boxes. Doesn't stop testing. Will fix files retrospectively later.  
 Occasional visits to help out with a few boards failing tests there.

Testing at RAL is keeping pace (just).  
 More details are in Production [spreadsheet](#).

ORx

eX are now short of 325 pcs for final assembly run ~ 40 boards.

Unfortunately the final batch of new Opto's (120 pieces = 15 board's worth) from Geneva are delayed and will not arrive before May?  
 But we do have in hand some Opto's which can be recovered from existing FEDv1 prototype boards. We have about 30 pieces which are already removed. We plan to send the remaining prototype boards to eX to remove the rest.  
 This should give us about another 10 board's worth.  
 That leaves about the final 15 boards out of 500 that we will not have Optos for.

Boards for which Optos are unavailable should still go through all the test procedures at eX. All tests BScan , VME except for the last analogue test ought to pass as normal. Some boards could be kept until the new batch of Optos do arrive and can then be assembled and final analogue test completed.  
 Leaving 15 unpopulated boards we will accept as long as they pass all the other tests. SLINK could be

tested with Fake Frames.

### **SLINK Transition Card**

Basic connectivity tests with FMM with slow throttle signals at 904 over a weekend were successful. Remaining 430 boards have now been ordered. Expect back in May. Then need testing at RAL.

Only change from first 70 cards is RJ45 (cable to FMM) replaced by shielded connector. This can be linked to Gnd point on card if it proves necessary, but it won't be done otherwise.

### **CERN Integration :**

Vertical slice Tests of FEDs with final TTC cards (TTCci) and FEC working. Tests with APVe also started. Still to do with FMM fast throttle.

Tests of 16 FEDs to FRLs in mobile crate in progress. Single links working. Merged links giving errors (at low rates?)

Rack mounted PCs installed with crates in B904. See Rack layout diagram from Oz.

Racks in place for production boards at B186 for Tracker Integration. Expect to move large nr FEDs in March?

Adding VME J0 backplane cards to 904 crates for multiple VME FPGA reprogramming as for Pt5 final system.

### **Firmware :**

FE VHDL Psedo Random data added to Fake frames.

Observed some odd behaviour with setting of some FE registers eg Super mode takes 2 attempts? Could be fedDebug software. Look with Chipscope.

BE will add additional DAQ info word in header.

BE Added test register for generating Throttle signals (used for FED-FMM validation).

BE fixed header store of changing PipeAddr (simple bug was introduced when we added Fake modes)

Fast Init added to VME.

New VME 2100032B as standard release (needed for Compact Flash reprogramming via VME using CAEN)

SPY channel capture of Frames. Changes for Delay FPGA is next task at RAL.

### **Software:**

Fast Pedestal loading with multiple serial commands and BLT writes is working.

Low level Software status (see report by Jo).

Standalone utility for reprogramming CFlash over VME being developed at RAL. fedDebug tool does it now.

**Other Items:**

Half of electronics Test lab at RAL is being taken over by Space Science.

FED test area is being slightly rearranged, but will remain in place until end of 2006.

PPARC commissioned photographer spent a day in RAL test lab. Photos available.

eXception visit to CERN to collect CMS Gold award went very well. Photos available.