

Project Monitor Form

Project: CMS FED Date: Thursday 22 April-2004	PMF number: 45 Sheet: 1 of 2
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Project Implementation phase.

FEDv1:

FED Status:

All analogue channels on all 6 new boards are now working.
Minor repairs were needed on some channels. Usually hand soldered term resistors or DAC pins.
Comprehensive production report received from DDi.
Nr 14 sent to CERN on 2nd April.
Occasional problems with TTCrx reset finally identified. Due to misunderstanding of document. JTAG Reset line was tied high instead of low. Simple hardware fix, which has been done on all boards at RAL.

System Tests

Operation with TCS throttle signals @40MHz verified at Imperial
Temperature tests show OptoRx and ADCs running hot.
Fan tray positioning in our crates is far from optimal. Plan to test in Atlas crate with 9 x fan tray.
Need to investigate other measures.

FEDv2

Draft FEDv2 schematics completed, but still to be reviewed.
PCB layout changes in preparation.
Darren Ballard will carry out the work, as Chris Day is likely to be occupied with Atlas job for several weeks.
Replacement for QDR identified. Samples on order.
Decision to change package or buy up VREF to be made soon.
VME EPROM JTAG programming lines to be brought to J0 with cable chained on back plane.
Reprogram from laptop or permanently mounted PC.