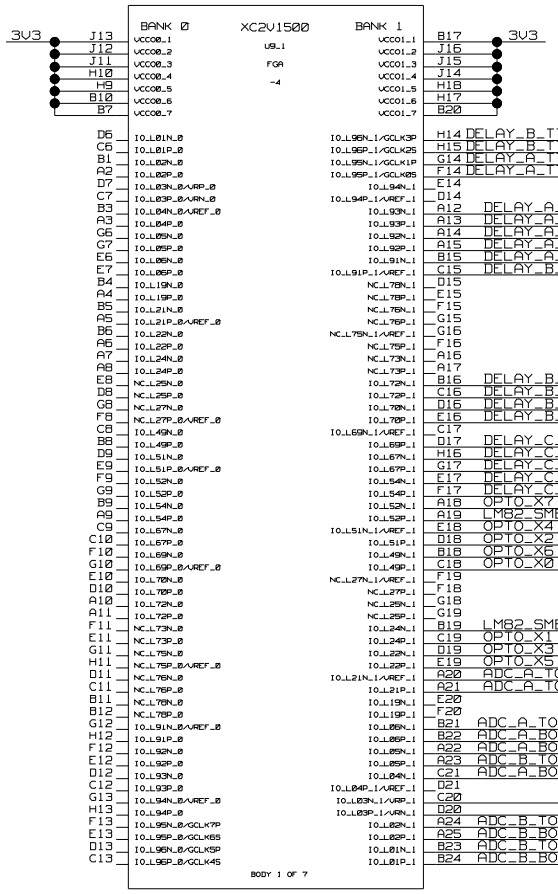
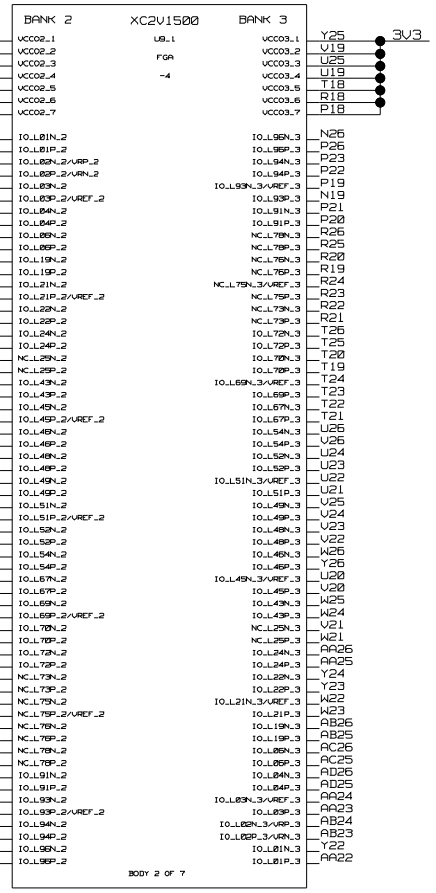
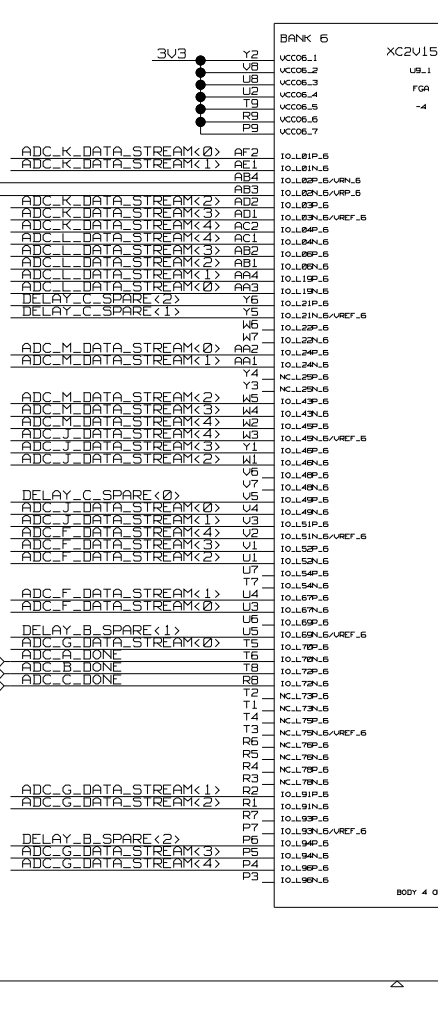
A | 2 -TE-0070-020<20>A | THIS DRAWING CONFORMS TO B.S. 3939







DATA_STREAM<0..3> OUT

ADC_A_DATA_STREAM<0..4> IN
ADC_B_DATA_STREAM<0..4> IN
ADC_C_DATA_STREAM<0..4> IN
ADC_D_DATA_STREAM<0..4> IN
ADC_E_DATA_STREAM<0..4> IN
ADC_F_DATA_STREAM<0..4> IN
ADC_G_DATA_STREAM<0..4> IN
ADC_H_DATA_STREAM<0..4> IN
ADC_J_DATA_STREAM<0..4> IN
ADC_K_DATA_STREAM<0..4> IN
ADC_L_DATA_STREAM<0..4> IN
ADC_M_DATA_STREAM<0..4> IN

DELAY_A_SPARE<0..3> IN
DELAY_B_SPARE<0..3> IN
DELAY_C_SPARE<0..3> IN

TP11_1 GND
TP12_1 GND

A	13-11-02	-----	S. TAGHAVIRAD	S. TAGHAVIRAD	S. TAGHAVIRAD	
ISSUE	DATE	MOD NO.	DRN BY.	CHKD.	APPD.	STATUS

USED ON CMS

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RUTHERFORD APPLETON LABORATORY, CHILTON, OXON OX11 0QX.

TITLE

PC3205M CIRCUIT

CMS TRACKER FED v1

FRONT END FPGA

A 2-TE-0070-020<20>A

TOTAL NO. OF SHEETS 95

A 2 -TE-0070-020<20>A