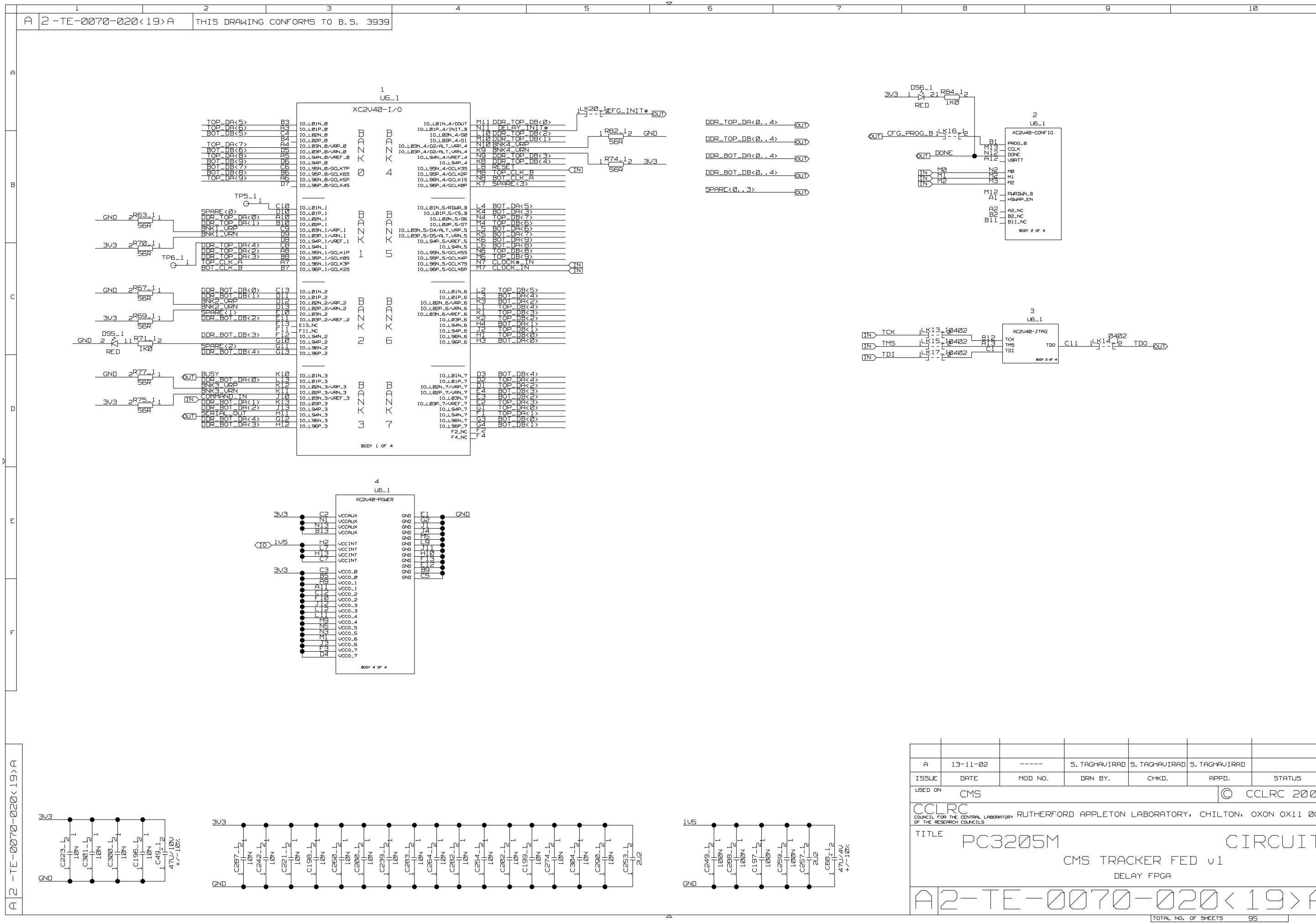
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2-TE-0070-020<19>A
THIS DRAWING CONFORMS TO B.S. 3939
U6_1
XC2V40-I/O
BODY 1 OF 4
U6_2
XC2V40-CONFIG
BODY 2 OF 4
U6_3
XC2V40-JTAG
BODY 3 OF 4
U6_4
XC2V40-POWER
BODY 4 OF 4
DS5_1
DS6_1
RED
CFG_INIT#
CFG_PROG_B
DONE
TCK
TMS
TDI
TDO
BUSY
COMMAND_IN
SERIAL_OUT
DDR_TOP_DA<0..4>
DDR_TOP_DB<0..4>
DDR_BOT_DA<0..4>
DDR_BOT_DB<0..4>
SPARE<0..3>
GND
3V3
1V5
CCLRC
COUNCIL FOR THE CENTRAL LABORATORY OF THE RESEARCH COUNCILS
RUTHERFORD APPLETON LABORATORY, CHILTON, OXON OX11 0QX.
TITLE
PC3205M
CIRCUIT
CMS TRACKER FED v1
DELAY FPGA
A
13-11-02
S. TAGHAVIRAD
ISSUE
DATE
MOD NO.
DRN BY.
CHKD.
APPD.
STATUS
USED ON
CMS
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2-TE-0070-020<19>A
TOTAL NO. OF SHEETS 95