

CMS Tracker FED

CMS Tracker Two Weekly Meeting

Design Update

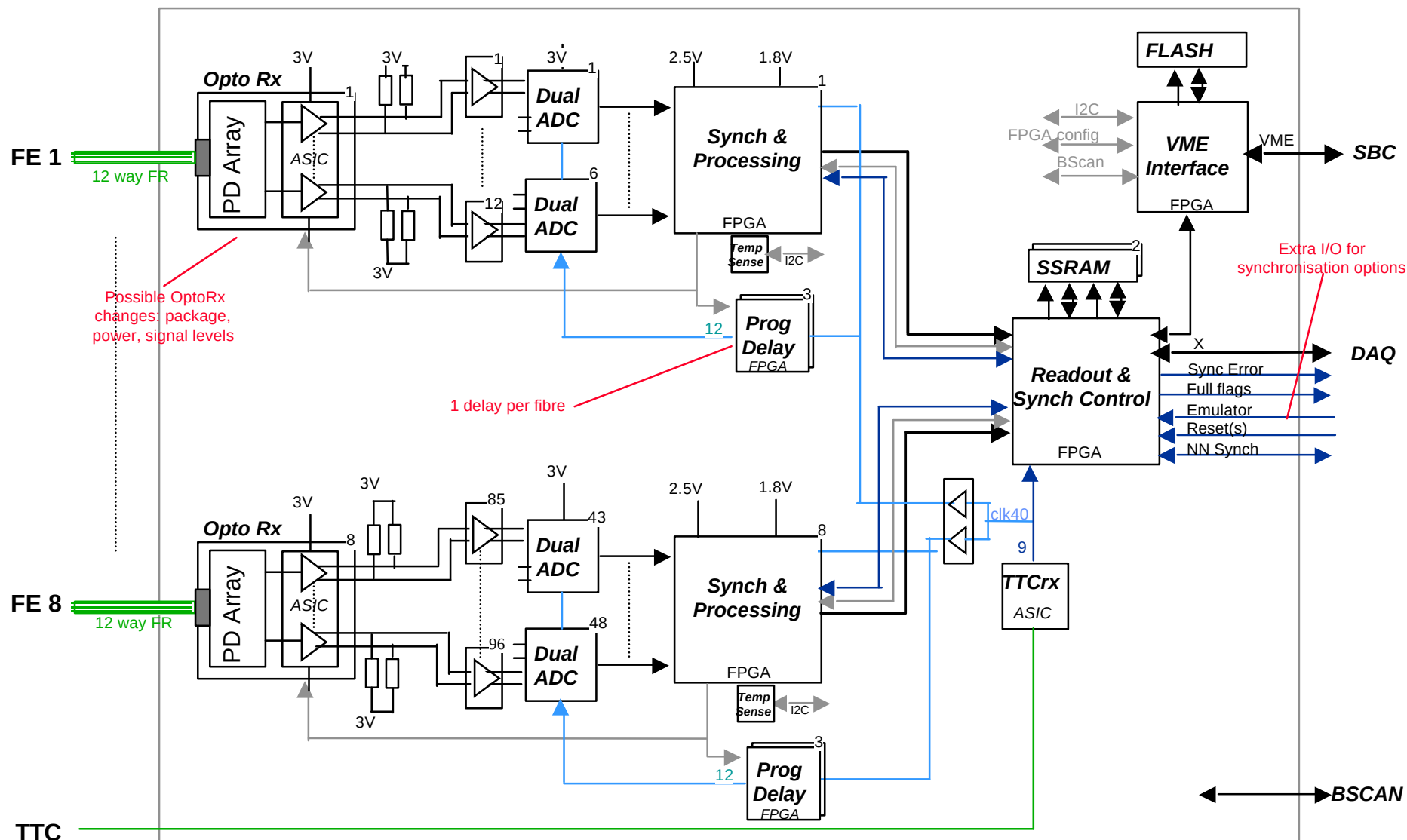
Rob Halsall et al

18 July 2001

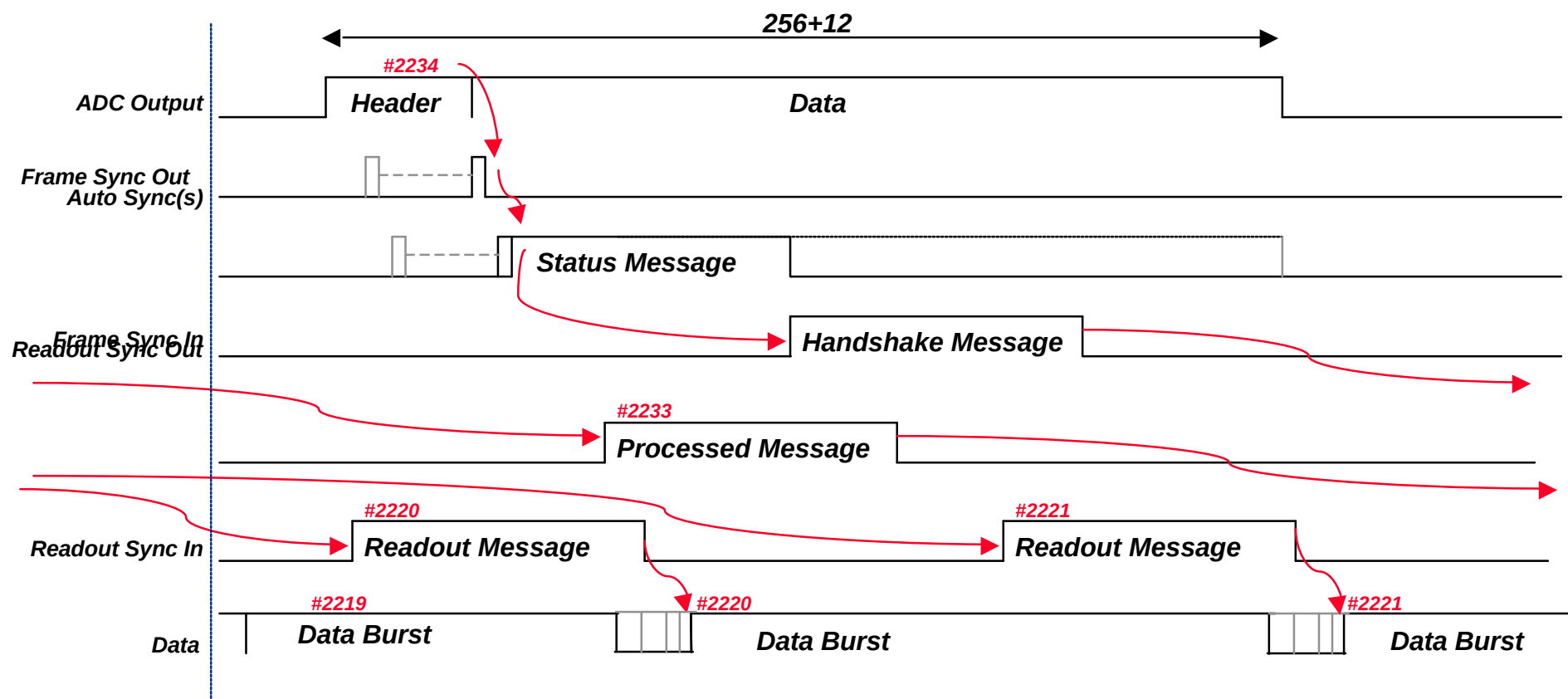
RAL

CMS Tracker FED

96 ADC Module



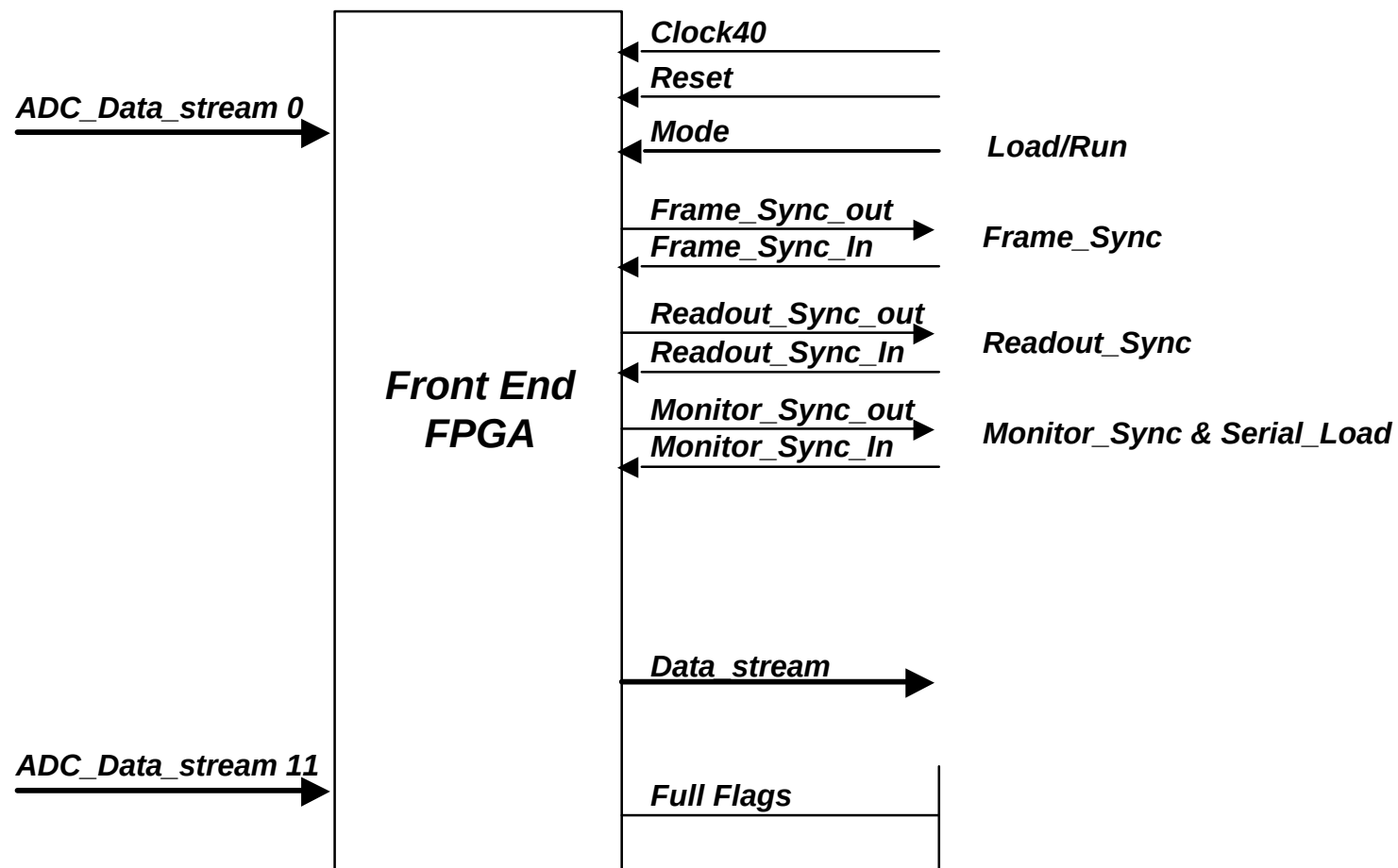
CMS Tracker FED System Timing





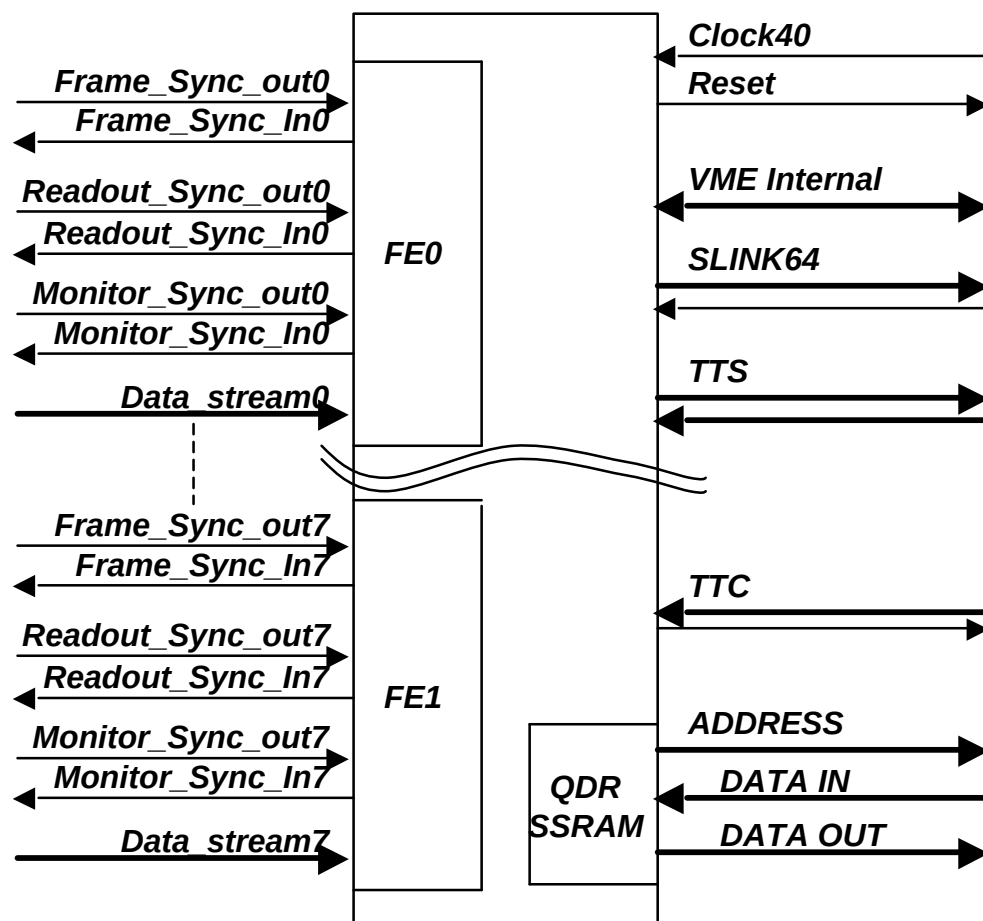
CMS Tracker FED

Front End FPGA



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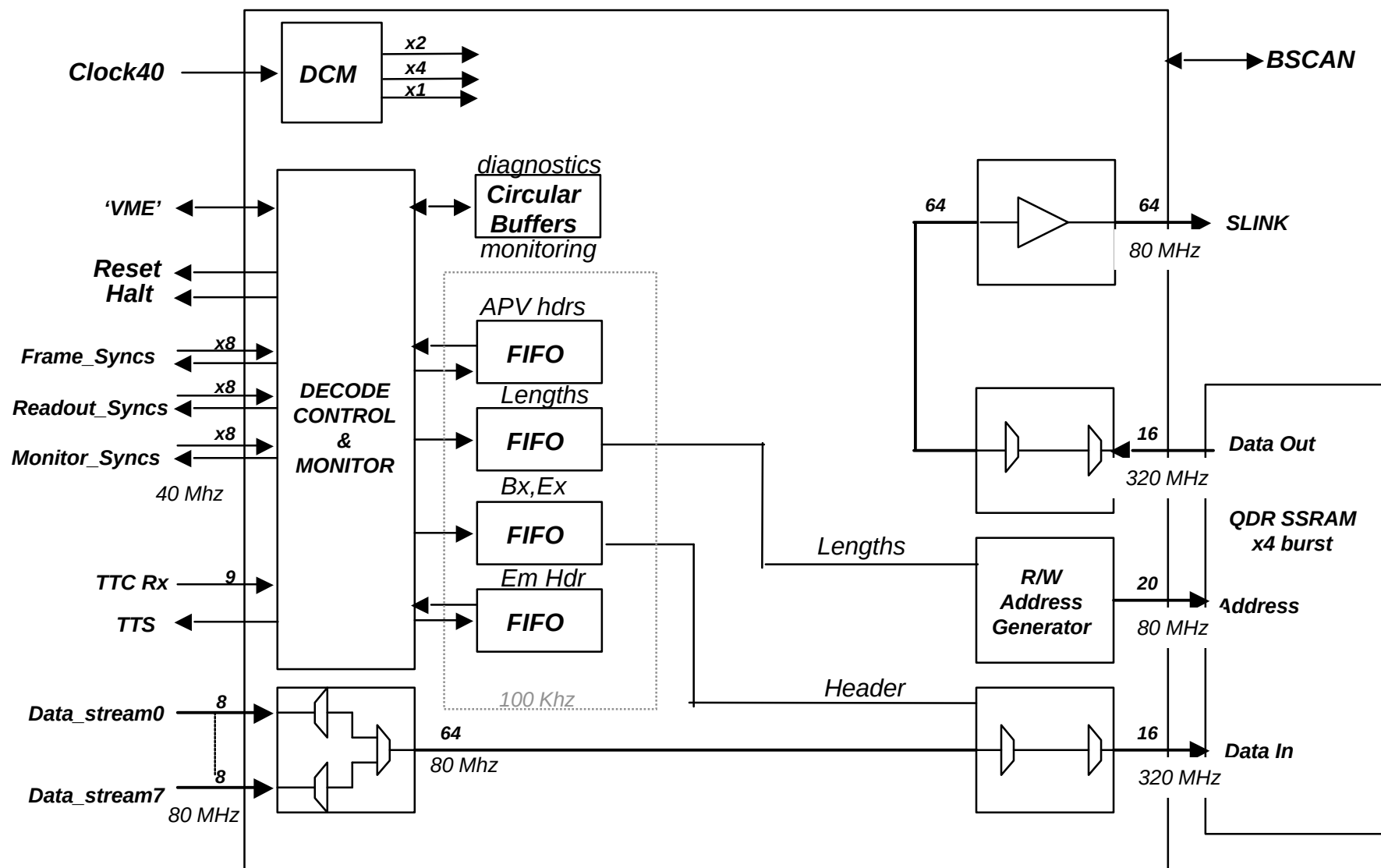
Back End FPGA





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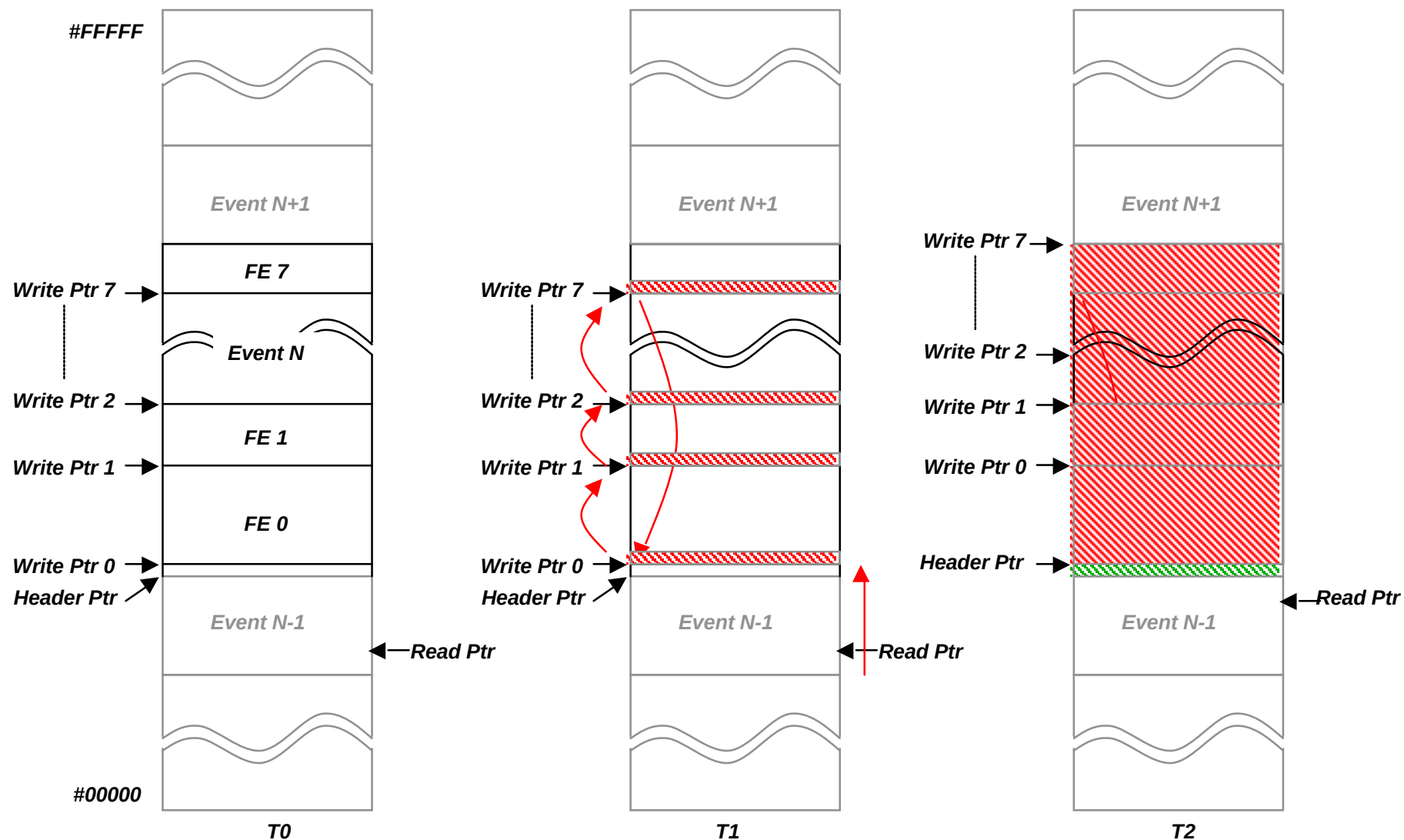
Back End FPGA





CMS Tracker FED

Back End FPGA





CMS Tracker FED System Overview

Board Layout

96 ADC Channels

